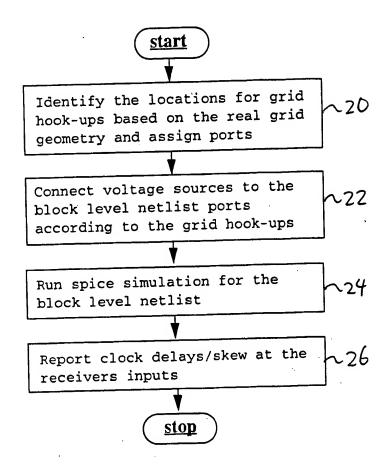
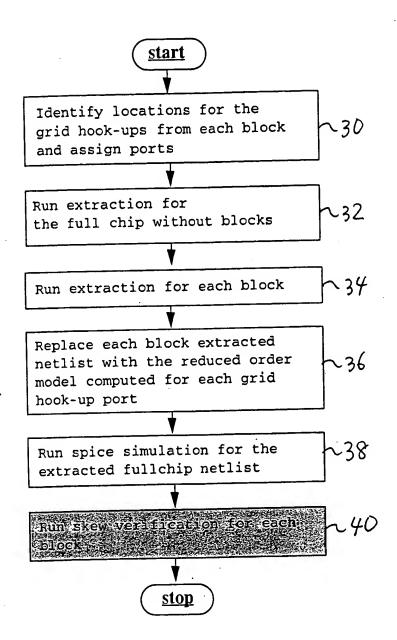


F1G. 1

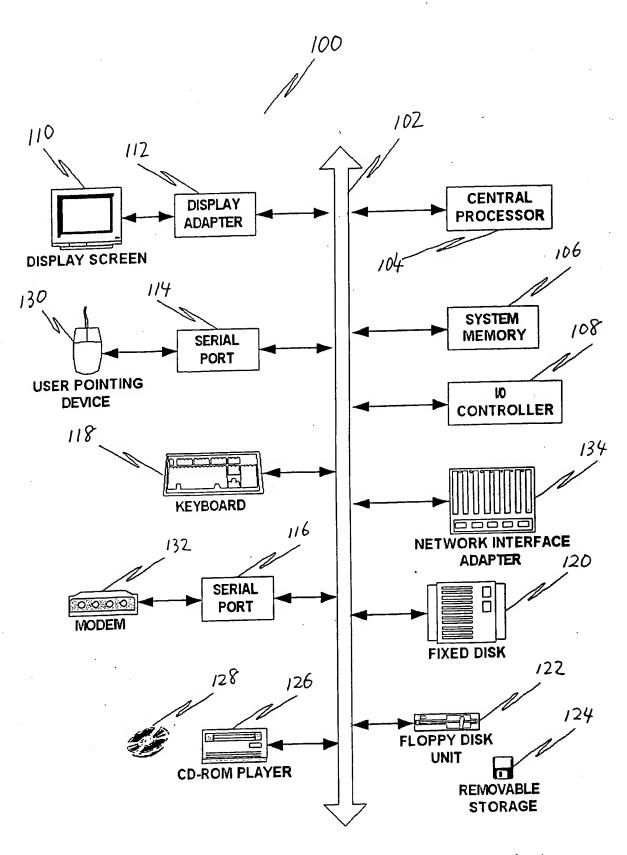


PRIOR ART

F19. 2



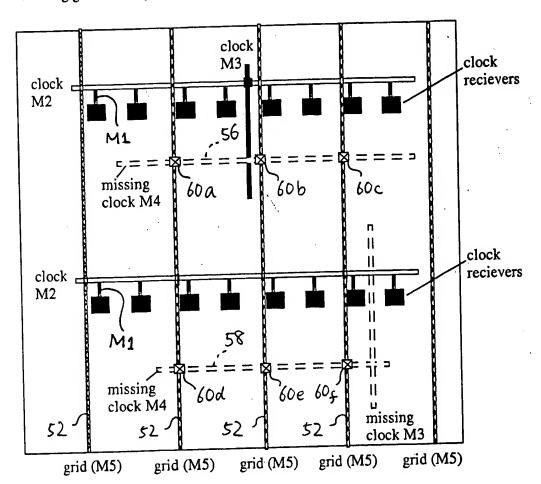
F19.3



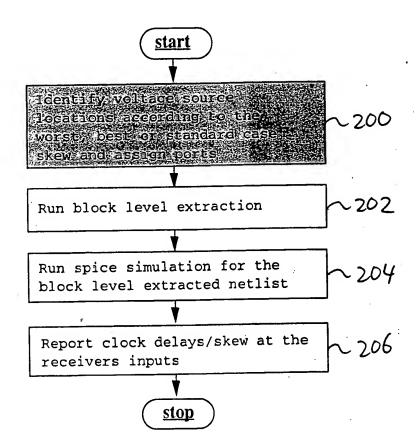
F16.4

50 سے

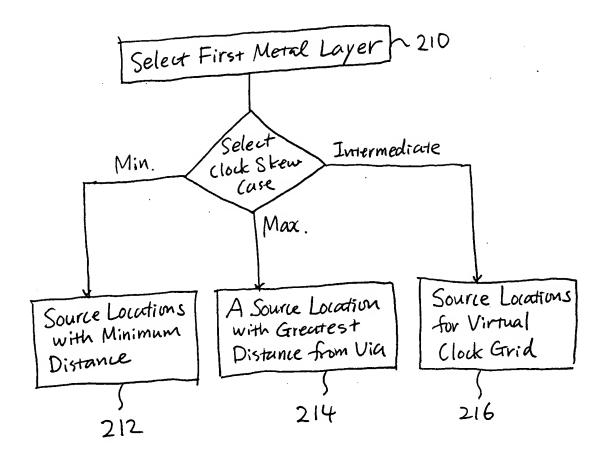
missing grid hook-ups



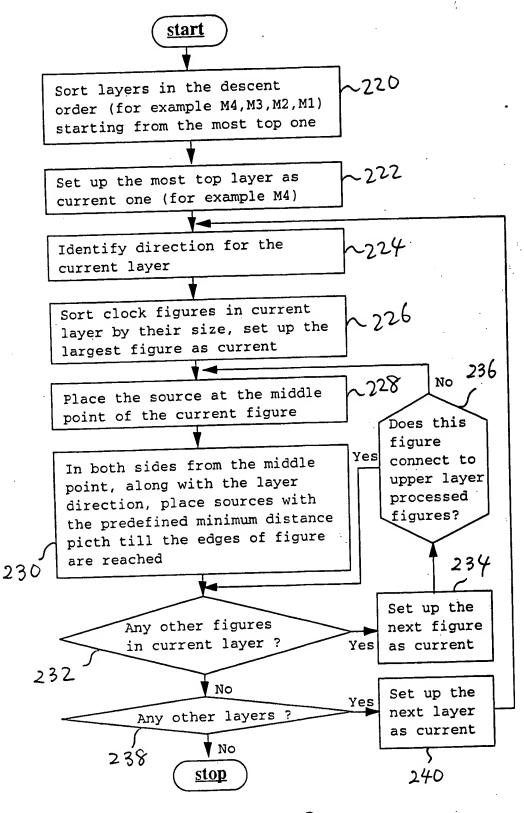
F19.5



F1G. 6

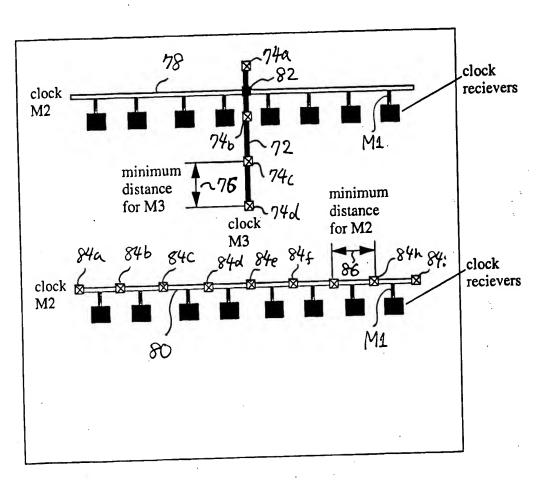


F14.7



F19.8

clock source locations for minimum skew



F19.9

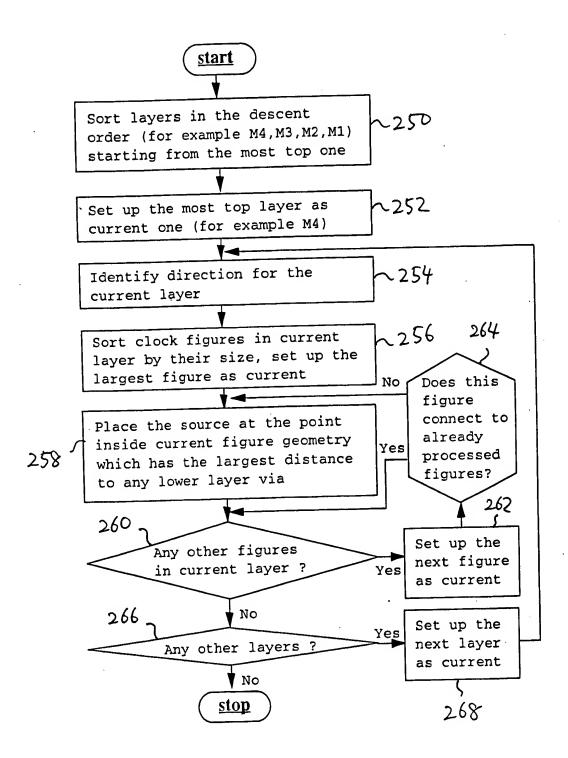
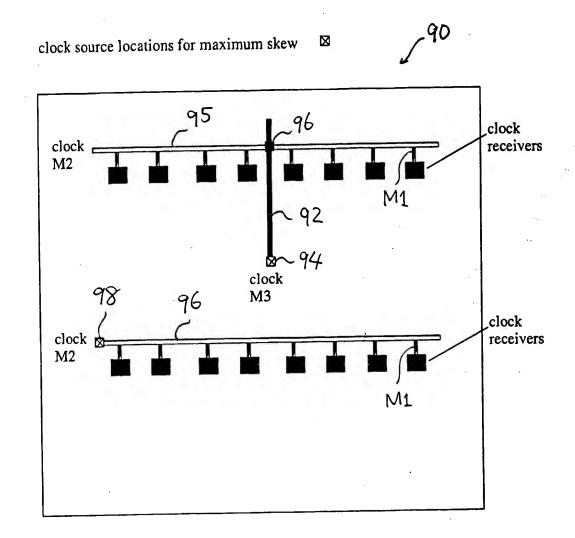
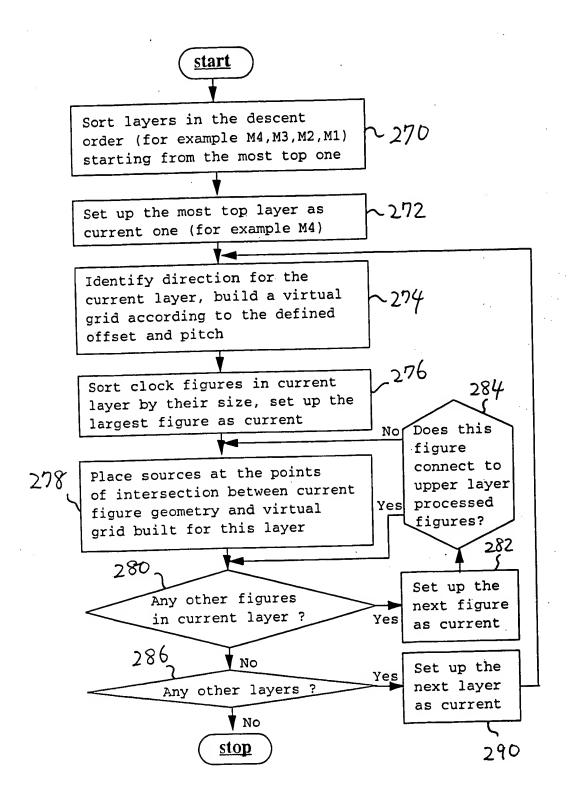


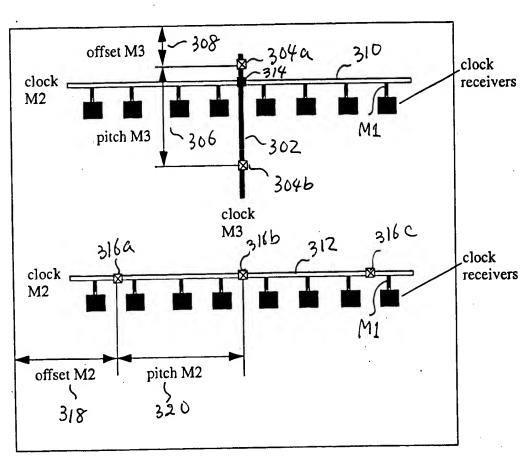
FIG. 10



F1G. 11



F19.12



F1G. 13

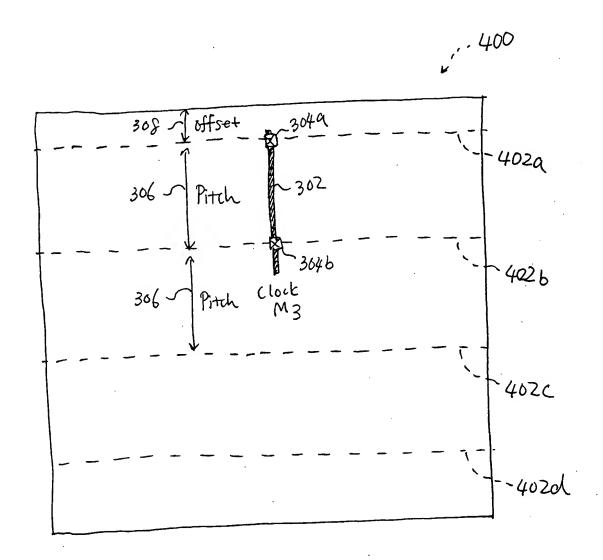
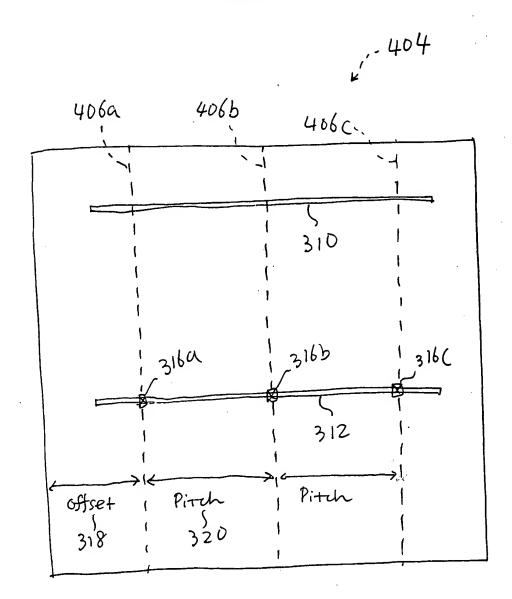
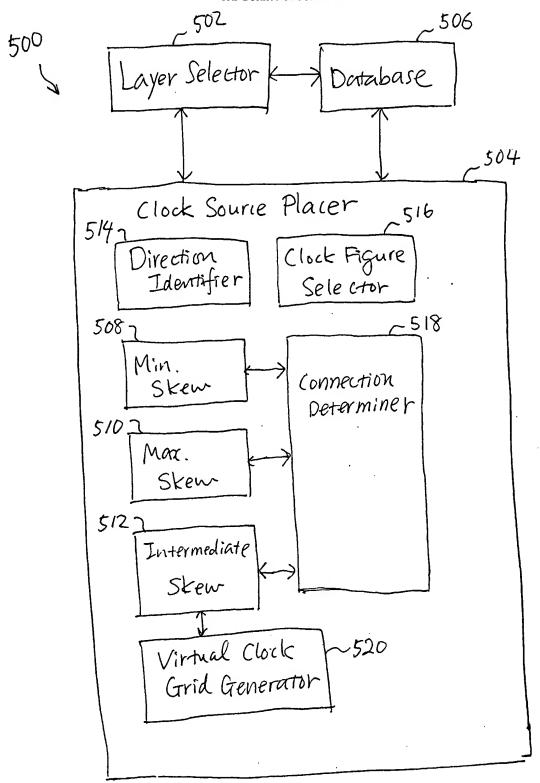


FIG. 14



FLG. 15



F19.16